

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Gajadharsing et al.
App. No	: 10/551,324
Filed	: October 18, 2005
For	: SEMICONDUCTOR TRANSISTOR (DMOS) DEVICE FOR USE AS A POWER AMPLIFIER
Examiner	: Sarah Kate Salerno
Art Unit	: 2814
Conf No.	: 7460

RESPONSE TO APRIL 16, 2009 FINAL OFFICE ACTION**Mail Stop AF**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Office Action mailed April 16, 2009, Applicant respectfully submits the following:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 4 of this paper.